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There are *mass applications* that require significant increase in compute density

- There is nothing as general-purpose as physics!
- Visual computing is a proxy of this much larger class (RMS)

These applications are not linear extensions of existing usage

• Optimal platform for such apps should not be linear extension either

There is a significant performance difference between a *brute-force* CMP Vs. a *smart* CMP targeted for this class

• There is significant opportunity for silicon differentiation

These apps will likely be the driver for most future technology vectors

Programming to processor to memory technology

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